

ABOUT THE INSTITUTION

Kalasalingam Academy of Research and Education (formerly Arulmigu Kalasalingam College of Engineering) was established in the year 1984 by the Kalasalingam Anandam Ammal Charities. “Kalvivalal” Thiru. T. Kalasalingam, veteran freedom fighter, is the founder chairman. The founding philosophy of the institute is to “Empower Youth”, for development through technology. The University offers 14 UG courses, 29 PG courses and PhD programs in all disciplines. Our University is the first institution in India to offer the 6 years Integrated B.Tech course in Engineering for the Speech & Hearing-Impaired Students. The University has signed MoU with many Foreign Universities, Research Organizations. Our University is accredited by National Assessment and Accreditation Council (NAAC), Bengaluru with 3.11 on a 4-point scale.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering was instituted in the year 1994 to start a 4-year B.E. degree programme affiliated to the Madurai Kamarajar University by the then Arulmigu Kalasalingam College of Engineering. The first batch of students graduated from the department in the year 1997. A post graduate programme in ‘Digital Communication and Networking’ was introduced in the year 2001. Both the programmes affiliation were shifted to Anna University in 2002 by the Tamil Nadu Government. The department was recognized as research Centre in 2003 to offer Ph.D. programmes in Electronics and Communication Engineering. In the year 2004, approval was obtained for introducing the second post graduate programme in ‘VLSI Design’. The department became a part of Kalasalingam Academy of Research and Education (Kalasalingam University) after the institution’s Deemed-to-be-University status in 2006. Since, 2015, there is a third post graduate programme in Mobile Communication and Computing.

After 2007, to cope-up the changes in technologies the department is having major changes in its programmes syllabi every 3 years. The B.Tech. Programme got provisionally accredited by National Board of Accreditation (NBA) under Tier-I category as

per Washington Accord in the year 2015 and 2018. The University has been accredited by National Assessment and Accreditation Council (NAAC) with ‘A’ grade with GPA of 3.11 out of 4-point scale.

ABOUT THE FDTP

The objective of the FDTP is to bring together experts from Industries, Academic and other user communities on a common platform to discuss and share their expertise and ideas about the technologies & recent trends in the field of Analog Integrated circuits.

COURSE COVERAGE

The main focus will be on B.Tech - ECE 4th semester syllabus of Analog Integrated Circuits.

The following topics will be covered:

- Basics of Operational Amplifiers
- Applications of Operational Amplifiers
- Analog Multiplier and PLL
- Application of PLL for AM and FM detection
- Analog to Digital & Digital to Analog converters
- Waveform generators and Special function ICs

ABOUT REGISTRATION

- Number of seats is restricted to 25 only.
- No Registration fees.
- Selection on first come first served basis.
- Preference will be given to the young faculty based on eligibility criteria.
- Selected candidates will be intimated by e-mail only.
- Send the duly filled in Registration forms to the Coordinator(s) **by post/mail on or before 7th November 2018**

IMPORTANT DATES:

Submission of Application : **07.11.2018**

Intimation of Selection : **08.11.2018**

Confirmation by Participants : **09.11.2018**

Date of FDTP: 12.11.2018 to 24.11.2018



TWO WEEKS FDTP

On

“ANALOG INTEGRATED CIRCUITS”

12th – 24th, November 2018

Organized by

**Department of ECE,
School Electronics and Electrical
Technology(SEET), KARE**

In Association



with

**IETE Students Chapter, ECE, KARE
and
Department of ECE
KALASALINGAM INSTITUTE OF
TECHNOLOGY**

**Two Weeks FDTP on
Analog Integrated Circuits
12th – 24th, November 2018**

REGISTRATION FORM

1. Name:
2. Designation:
3. Qualification:
4. Age & Gender:
5. Professional Experience (in Years)
Teaching:
Industry:
6. Did you attend this course earlier? Yes/No
7. Address of the College/Institution:
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.....
.....
8. Research Interest:
9. Phone (Mobile):
10. E-Mail:
11. Accommodation Required? Yes/No

DECLARATION

The information provided by me is true to the best of my knowledge. I agree to abide by the rules and regulations governing by the FDTP. If selected I shall attend the program for the entire duration.

Signature of the Participant

Date:
Place :

SPONSORSHIP

Dr./Mr./Mrs. _____

is an employee of our institution. He / She will be permitted for the above FDTP, if selected.

Signature of the Principal with Seal

For further details, contact:

**The Coordinators,
FDTP on
" Analog Integrated Circuits ",
Department of ECE, SEET, KARE
Anand Nagar, Krishnankoil – 626 126**

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y.muneeswaran@klu.ac.in

**VENUE: 4th Floor-Seminar Hall,
Dept of ECE, KIT**

ORGANIZING COMMITTEE

CHIEF PATRON: Dr. K. Sridharan
Chancellor, KARE

Dr. S. Arivazhagi
Pro-Chancellor, KARE

Dr. S. Shasi Anand
Vice President (Academics)
KARE

Dr. S. Arjun Kalasalingam
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KARE

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HOD/ECE, SEET, KARE

Dr. A. Muthukumar, Ph.D.,
Associate Professor / ECE,
SEET, KARE

CO-ORDINATORS:

**Dr. K. S. Dhanalakshmi,
AP III /ECE,SEET,KARE**

**Mr. V. Muneeswaran,
AP I/ECE,SEET,KARE**