

For the first time in the country Assure Yourself
A paid internship and a great career at TESSOLVE



Two Year Full Time M.Tech Program in VLSI Design, Test and Manufacturing



Jointly Offered By
KALASALINGAM UNIVERSITY, KRISHNANKOIL
and
TESSOLVE SEMICONDUCTOR, BANGALORE

The Kalasalingam University in partnership with the Tessolve Semiconductor, Bengaluru (a leading Semiconductor Engineering Services Company in India) offers a unique M.Tech programme in VLSI Design, Test and Manufacturing.

This four-semester programme is aimed at creating a pool of responsible and resourceful technical professionals for the VLSI Design, Test & Manufacturing sector. It is designed to impart in-depth practical knowledge and expertise to the students through innovative learning, supported by hands-on training by high caliber research and technology leadership at Tessolve Semiconductor.

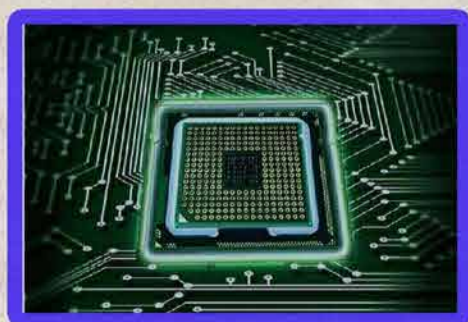
Historically the subject of Semiconductor testing has fired the imagination of many electronic design engineers. However the vast increase in on-chip circuit complexity arising from the evolution of LSI and VLSI technologies has brought this subject into rightful prominence, making it an essential part of the overall design procedure for any complex circuit or system. The manufacture of a majority of semiconductor products usually involves a rigorous design activity followed by detailed testing and evaluation of prototypes before volume production is begun. The ability to test and produce products as comprehensively as possible within a given budget and time is a challenging task.

The programme will also cover methodologies for design and implementation of first level electronic packaging technologies, materials, and equipment for the manufacturing of multi and single chip packages for VLSI, subsystems and Microsystems.

This Programme is unique in terms of its real-time and project based curriculum and industry specific course contents. The curriculum has been designed by Tessolve Semiconductor in close consultation with the Kalasalingam University anticipating the current and future industry trends.

Expertise and experience of renowned professionals at Tessolve Semiconductor is shared with students to provide them deep insight into technical developments and fundamentals through lecture classes and hands-on training.

Students also get an opportunity to work on live industry project under the Internship mode as a part of their Project Phase. This first-of-its-kind application-oriented programme provides students with a unique opportunity to handle highly sophisticated IC test infrastructure, advance design tools with resourceful design libraries and other laboratory facilities in order to provide complete focus on all aspects of VLSI design, test and manufacturing.



Courses in Curriculum

- Semiconductor Physics and Device Modelling
- Scripting Languages for VLSI Design Automation
- Digital Design
- SOC Architecture Design
- Microsystems and Microengineering
- Testing and Testability
- System and Device Packaging
- SOC Design and Verification
- Test Engineering
- VLSI Fabrication Technology
- RTL to GDSII
- Reliability Engineering
- IC Test Laboratory
- SOC Verification Laboratory



Unique Features of the Programme

- The programme has been CO-DESIGNED and CO-ADMINISTERED by KLU and TESSOLVE SEMICONDUCTOR.
- Stipend of Rs. 10,000/- per month will be provided for GATE-qualified students and University Rank holders during first three semesters.
- All students after successfully completing in campus requirements will be absorbed in Tessolve Semiconductor with a highly competitive package at the start of the 8 months internship. They will be placed in any of the global locations of Tessolve Semiconductor depending on their sub areas of training and projects fields.
- During the internship, the students will be paid Rs.25,000/- per month by Tessolve, Bangalore(after due monitoring).
- Specialized courses would be taught by global experts from Industry and US Universities in the field.
- Tie up with Banks for availing bank loans for all admitted students.

Eligibility

B.E/B.Tech., in ECE, EEE, EIE, ICE or equivalent, who have passed with first class are eligible to apply. Candidates who have no history of arrears/ GATE Scorer/ University Rank Holders will be given preferences.

Selection Process

Candidates will be selected based on online written test (Syllabus covers Digital System, Analog System, Basic of Circuit Theory, System Architecture) cum Interview.

How to Apply

For details, Please visit our website. <http://kalasalingam.ac.in>

Fees Structure

One lakh per semester.

Important Dates

Last date for submission of filled in Application	23.06.2015
Online written Exam *	12.07.2015
Publication of result/call for interview	15.07.2015
Commencement of classes	17.08.2015

*Candidates with valid GATE score are exempted from online exam

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
KALASALINGAM UNIVERSITY**

(Kalasalingam Academy of Research and Education)
Anand Nagar, Krishnankoil-626126, Tamil Nadu, India.

Email: info@kalasalingam.ac.in, Phone: 04563 – 289042/43/44/52, Fax: 04563 – 289322

